| | Type | L # | Hits | Search Text | DBs |
|---|------|-----|------|-------------|-----|
| 1 | BRS | L1 | 0 | sendrovitz-$.in. | US-PGPUB; USPAT |
| 2 | BRS | L2 | 120 | (local adj1 clock) and channel$1 and (fifo or first-in-first-out or (first adj4 out)) and ((sequence adj1 number) or (tag adj1 number) or (identif$5 adj1 number)) and memory | US-PGPUB; USPAT |
| 3 | BRS | L3 | 63 | 2 and (phase-shift$4 or (phase adj1 shift$4)) | US-PGPUB; USPAT |
| 4 | BRS | L4 | 19 | 3 and (pld or fpga or (programmable adj1 device)) | US-PGPUB; USPAT |
| 5 | BRS | L5 | 1 | "6542096".uref. | USPAT |
| 6 | BRS | L6 | 2 | "6750675".uref. | USPAT |
| 7 | BRS | L7 | 120 | 2 and transmit$5 and receiv$4 | US-PGPUB; USPAT |
| 8 | BRS | L8 | 97 | 7 and alignm$4 and synchroniz$5 | US-PGPUB; USPAT |
| 9 | BRS | L9 | 10 | 8 and ((logic adj1 circuit) or (logic adj1 block) or (logic adj1 cell)) | US-PGPUB; USPAT |
| 10 | BRS | L10 | 30 | venkata-$.in. | US-PGPUB; USPAT |
| 11 | IS&R | L11 | 2996 | ((326/37) or (326/38) or (326/41) or (326/46) or (326/47) or (326/82)).CCLS. | US-PGPUB; USPAT |
| 12 | BRS | L12 | 4 | 11 and (phase-shift$4 or (phase adj1 shift$4)) and alignm$4 and synchron$5 | US-PGPUB; USPAT |

| | Type | L # | Hits | Search Text | DBs |
|---|---|---|---|---|---|
| 13 | BRS | L13 | 208 | (data NEAR1 recover$5) and (clock NEAR1 recover$5) and (phase-shift$4 or (phase adj1 shift$4)) and alignm$4 and synchron$5 | US-PGPUB; USPAT |
| 14 | BRS | L14 | 47 | 13 and ((multiple adj1 channel$1) or (data NEAR2 group)) | US-PGPUB; USPAT |